



PATENT
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Client Reference No. F32292US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael J. UNDERHILL

Application No. 09/831,413

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For: ANTI-JITTER CIRCUITS

Art Unit: 2816

Examiner: M.T. Nguyen

AMENDMENTS TO SPECIFICATION AND CLAIMS
MADE IN RESPONSE TO OFFICE ACTION DATED JULY 3, 2002

IN THE SPECIFICATION:

Please amend the paragraph beginning on page 3, line 16, through page 4, line 3, as follows:

Figures 8(a) and 9(a) show anti-jitter circuits according to the invention including circuitry arranged to maintain the charge value of charge packets substantially constant. Figures 8(a) and 9(a) are circuit diagrams showing the anti-jitter circuits. Figures 8(b) and 9(b) show the input waveforms Op2 and the mean d.c. levels Op3 overlaid, and Figures 8(c) and 9(c) show an expanded detail of the overlaid waveforms. Figures 8(b) and 8(c) also show a voltage waveform Op4, and

Please amend the paragraph on page 11, beginning at line 11 through line 17, as follows:

Figure 10 shows a further embodiment of the invention in which inverted gate means in the form of a NOT gate U4 is used as a fast switching comparator. The switching level of the gate can vary appreciably with time and temperature; however, this can be controlled by